Fig. 1

INPUT MATCHING CIRCUIT

SOΩ

AMPLIFYING DEVICE

SOΩ

SOΩ

DEVICE

SOΩ

SOΩ

SOΩ

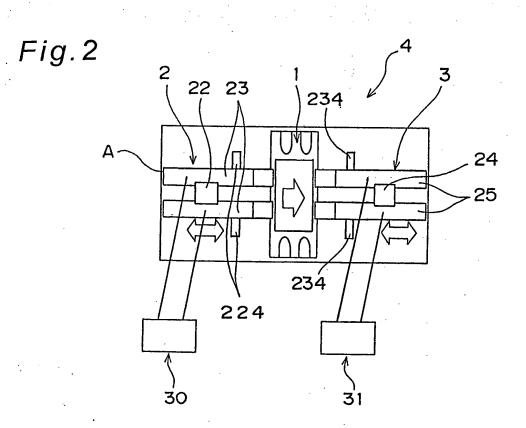


Fig.3

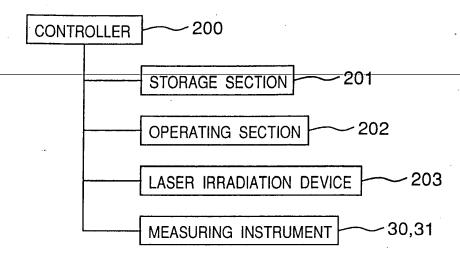
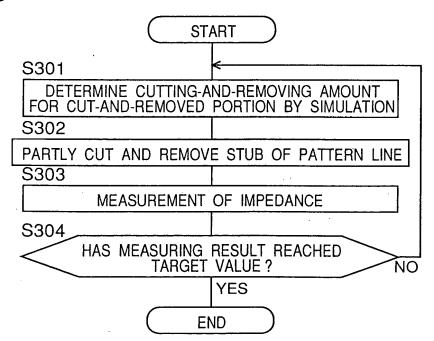


Fig.4



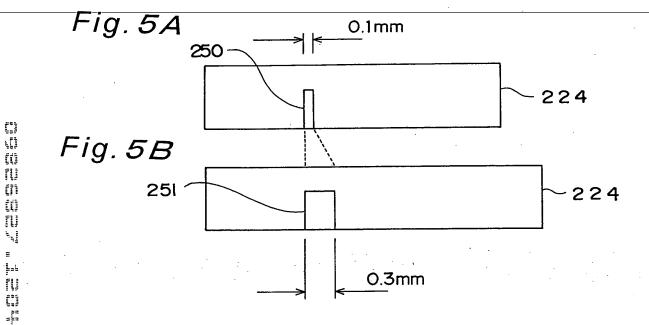


Fig. 6A

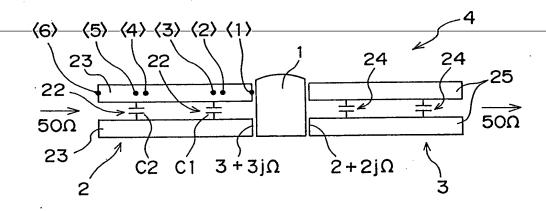


Fig.6B

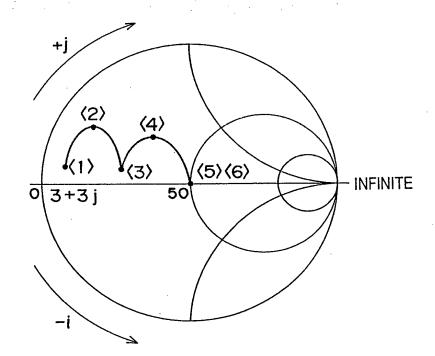
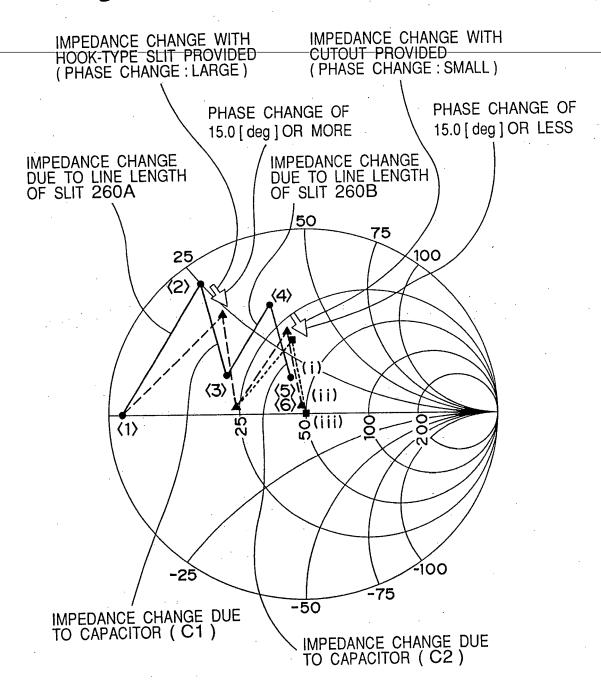


Fig.7



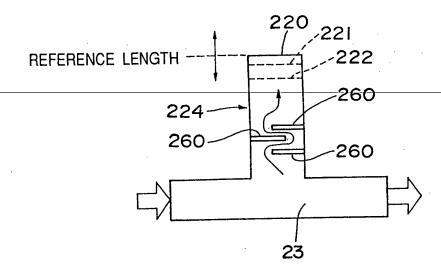


Fig. 9

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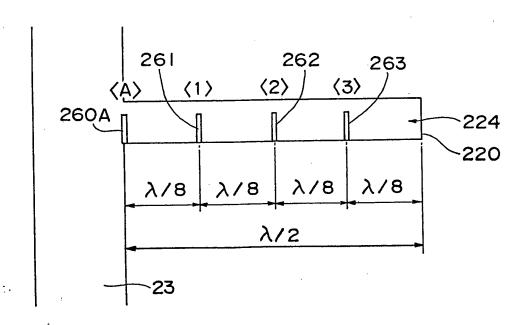


Fig. 10

25

(3)

(3)

(4)

(2)

(2)

(5)

(75)

(100)

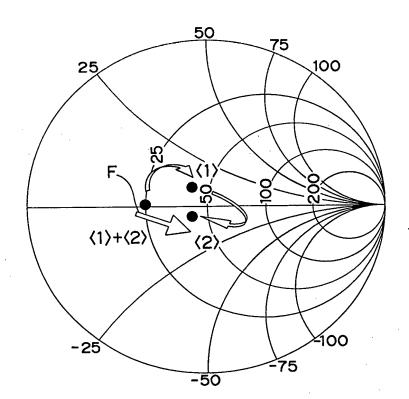
(75)

(100)

(75)

(100)

Fig. 11



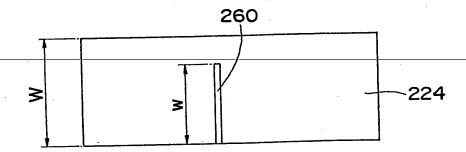
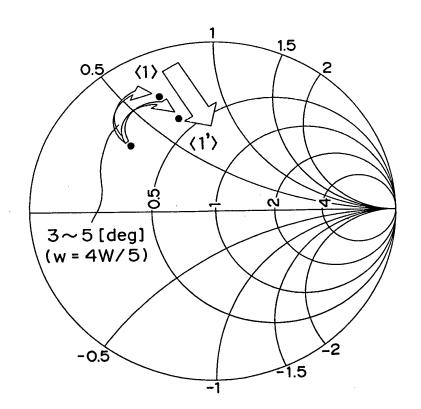


Fig. 13

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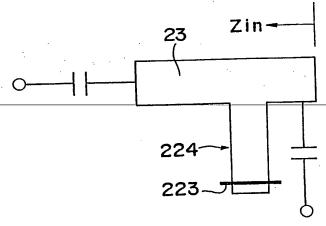
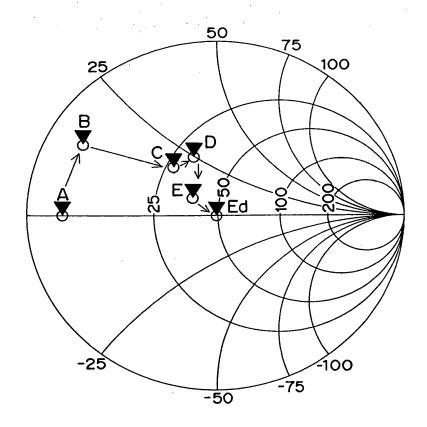


Fig. 15



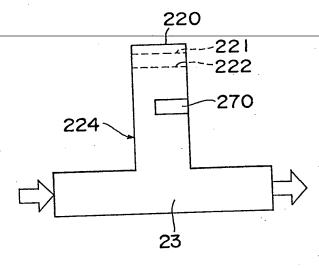


Fig. 17

Ar is brigh after and a proof after after the state of th

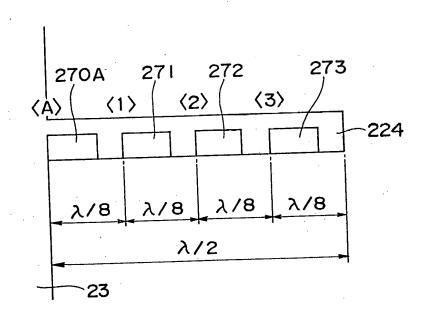


Fig. 18

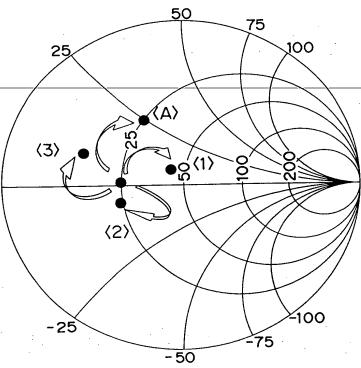


Fig. 19

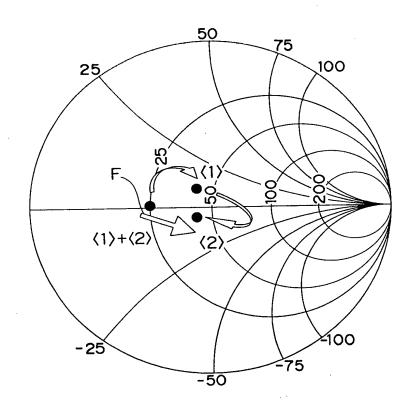


Fig. 20

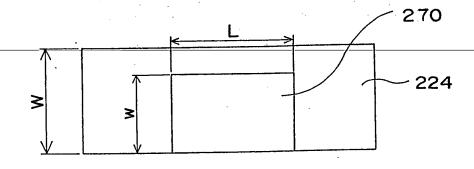
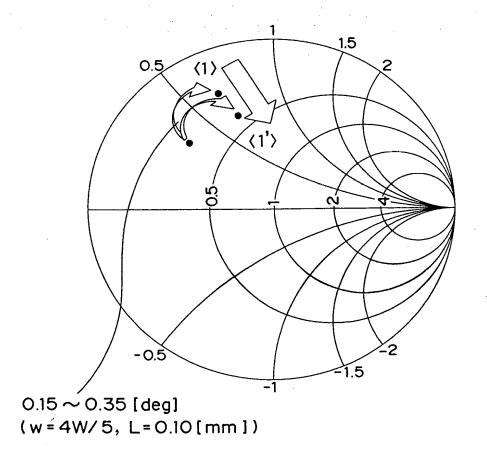


Fig. 21



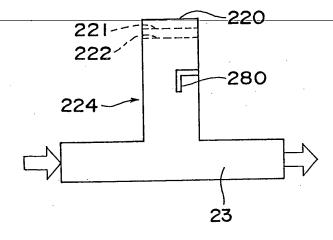
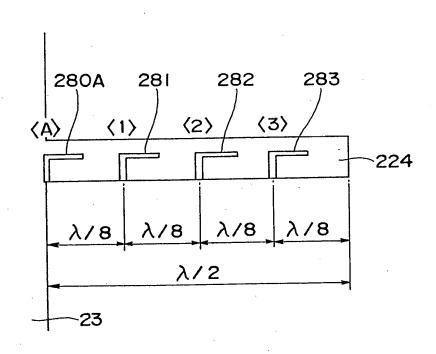


Fig. 23



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Fig. 24

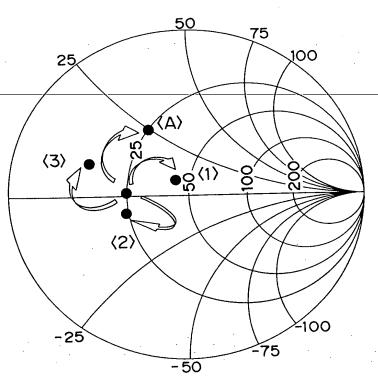


Fig. 25

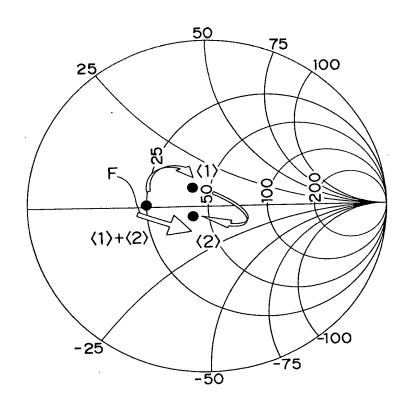


Fig. 26

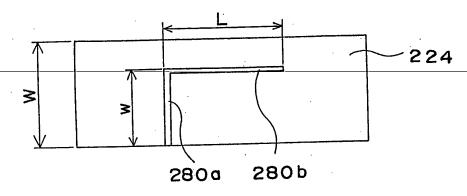


Fig. 27

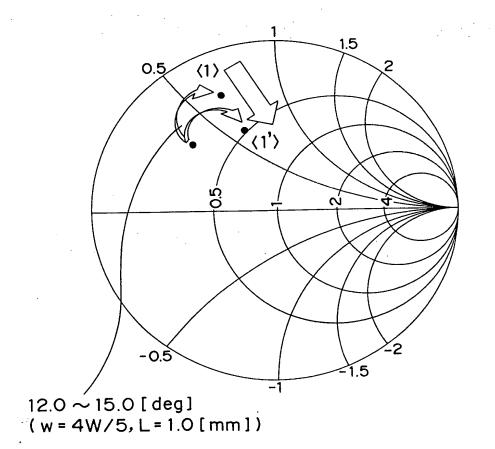


Fig. 28

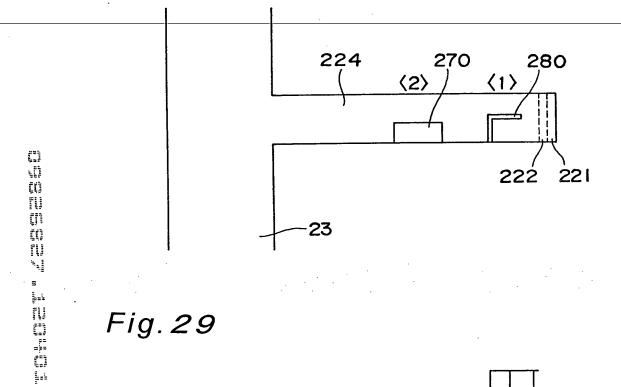
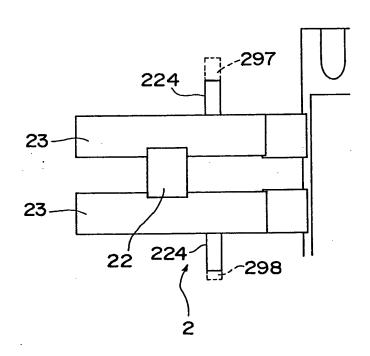


Fig. 29



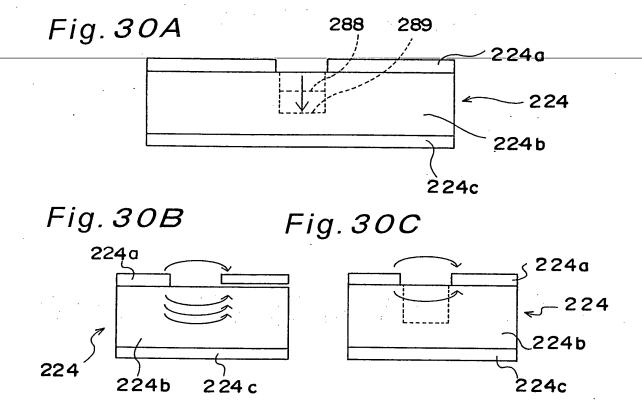


Fig. 31

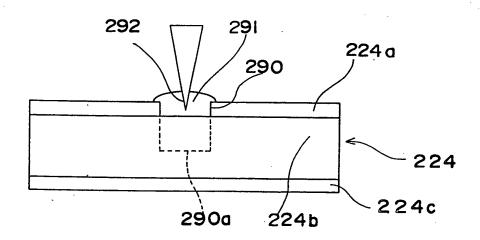


Fig. 32A

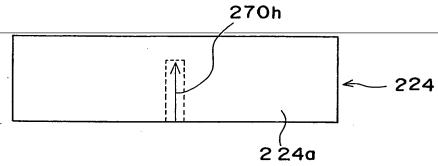
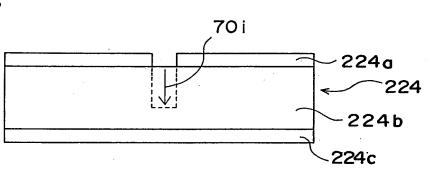


Fig. 32B



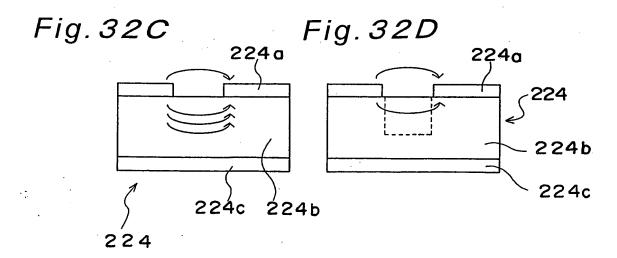


Fig. 33

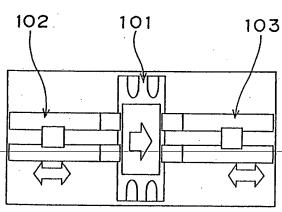


Fig. 34

H. 18 H. H. H. 18 Mark Arm. H. 18 H.

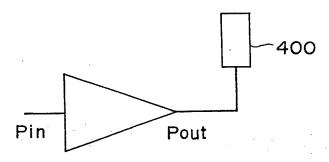


Fig. 35

